



THE ASSOCIATION OF SCIENCE
AND ENGINEERING TECHNOLOGY
PROFESSIONALS OF ALBERTA

Technical Examination Handbook

Electronics Engineering Technology

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Introduction

The **Technical Examination Handbook – Electronics Engineering Technology** has been prepared for electronics engineering technologists who are required to pass a technical knowledge exam to achieve registration within their respected technology associations. The handbook is designed to provide candidates with essential information regarding the examination.

Examination Information

Purpose of Examination

The purpose of the *Technical Examination – Electronics Engineering Technology* is to identify competent electronics engineering technologists who possess technical competencies in their discipline, as outlined in a discipline-specific competency profile. The goal is to protect the public by granting designations only to those professionals who have the skill and knowledge necessary to perform their job in a safe and competent manner.

Examination Format

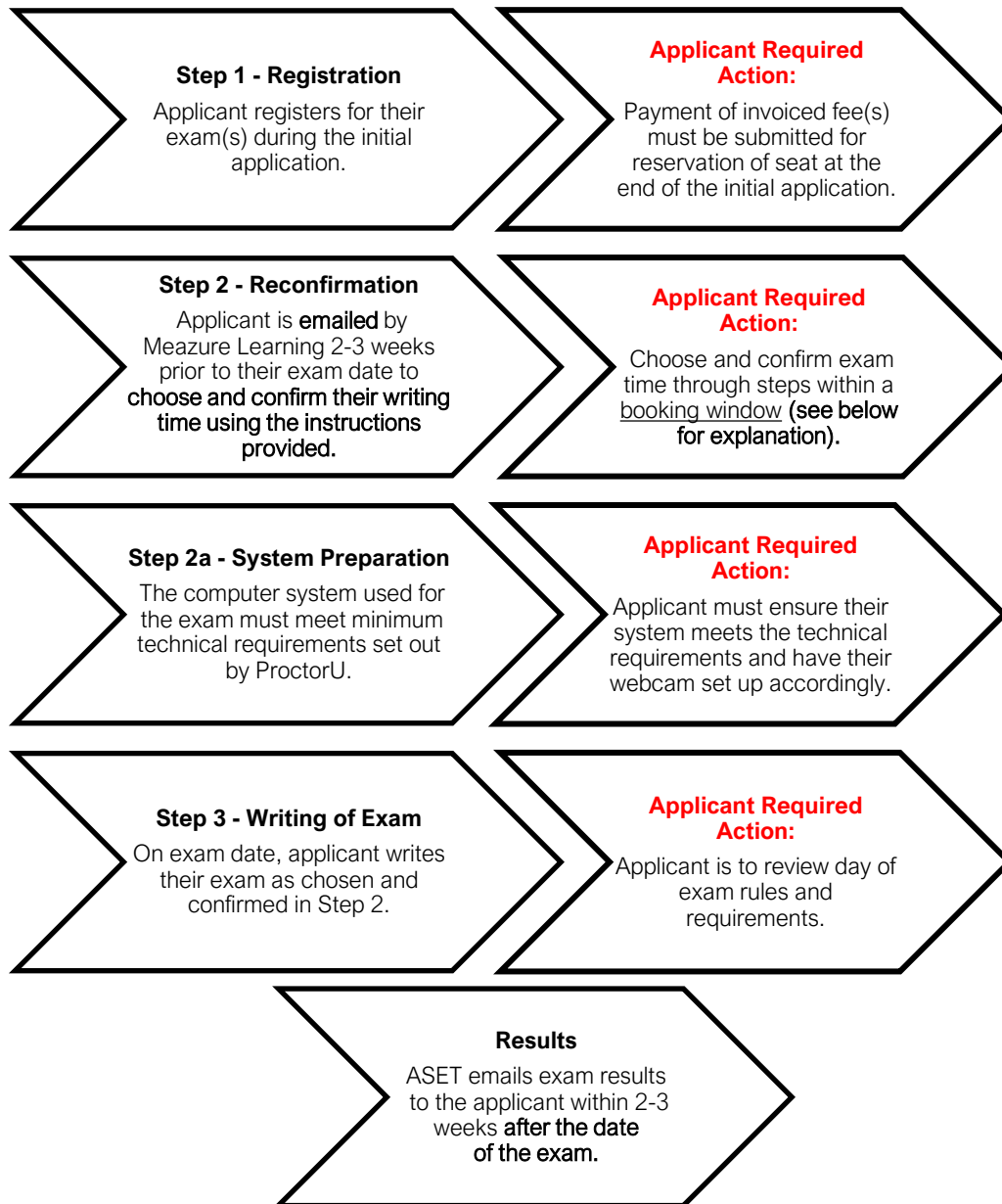
The examination consists of 110 multiple-choice questions, including questions with graphs, diagrams, and schematics and questions that require calculations. There are 10 experimental questions of the 110 that are not counted towards the candidate's exam score. These items are used for future exam development following the Embedded Field-Testing Method. Each multiple-choice question has four answer options, only one of which is correct. Exam questions vary in the level of cognitive difficulty. The exam is closed book, with a formula sheet provided as necessary and is three hours in duration.

Eligibility

To be eligible to write the exam, candidates must have obtained the minimum required technologist level experience to apply for their desired designation within their technology association. For this reason, students and T.T. registrants may not register to sit the exam.

Examination Registration

Applicants who are required to complete the Technical Examination will register for the examination at the time of application. Please see the ASET, CTTAM, or ASTTBC websites for current information on examination dates, registration processes and deadlines.



Booking Window

Two to three weeks prior to the exam, you will receive a booking email from Meazure Learning. This email will include the instructions on how to book a seat for your exam. We recommend watching this [quick video demonstration](#) on how to reserve your seat.

Exam Delivery

Effective January 01, 2026, the Technical Examinations will only be administered through a virtual proctoring service to ensure fair, accessible testing for all candidates.

This method of exam delivery uses a webcam with a virtual proctor to allow the candidate to write the exam from home. **Work computers and networks are no longer** permitted as there has been a significant increase in exam candidate technical issues (due to firewalls and restrictions on installing the necessary software to take the exam). To write the exam, it is mandatory to use a **personal device and network** that meets the requirements listed below:

Exam room requirements

- Quiet and private room; **if anyone enters the room during the exam other than the above the candidate, the exam results may be invalidated**
- Good quality lighting in the room; no bright lights or windows behind candidate
- Desk must be clear of any notes, books, or electronics with access to power outlets and internet connection (Ethernet plugged or high-quality password protected Wi-Fi)

Internet requirements

- A hardwired connection is preferred (wireless permitted but not encouraged)
- Internet speed must be a minimum of 10Mbps.
- Browser's pop-up blocker must be disabled (ensure this is done by following this help [link](#))
- Test internet speed using any of the following tests:
 - <http://speedtest.googlefiber.net/>
 - <http://beta.speedtest.net/>
 - <https://fast.com>

Computer requirements

- Laptops or desktop computers are permitted so long as they have the following minimum requirements:
 - <https://support.proctoru.com/hc/en-us/articles/115011772748-Equipment-Requirements>
 - PC users must have Windows 10 to align with the operating system Microsoft is supporting
 - No virtual machine desktops (must be on the host desktop), or tablets or smartphones
 - Candidates must source their own computers with the minimum specifications for the exam
 - Google Chrome **required** (can use Firefox, Safari, but Chrome is highly recommended)
 - Laptops must be plugged in during the exam to prevent the exam terminating early due to battery failure
- Functioning free standing or integrated webcam with microphone or independent microphone
- Only one computer monitor allowed

Guardian Secure Proctoring Browser requirements

- Candidates must download the Guardian Secure Proctoring Browser ahead of exam.
 - [Windows Download](#)
 - [Mac \(Intel\) Download](#)
 - [Mac \(M1\) Download](#)
 - To determine which Apple download to use, refer to this [support article](#).
- Google Chrome is highly recommended to download the browser

NOTE: It is the candidate's responsibility to ensure that the internet and computer requirements are met prior to the exam administration date. The **compatibility check** information will be located in your BOOKING CONFIRMATION email which is sent by Meazure Learning.

- Applicants must conduct a compatibility check using the computer they will use, in the room they will write in to test their connectivity prior to the virtually proctored exam. If they do not complete the compatibility check, there is no guarantee that the exam will happen. *The ProctorU exam program cannot be downloaded until the day of your exam so there is no way to check if it will work prior to exam day.*
- Meazure Learning has an [FAQ page](#) with a short tutorial video on the ProctorU program.

Exam Time and Confirmation

Applicants can expect an email from Meazure Learning **one (1) to two (2) weeks** prior to their registered exam date with instructions to confirm their seat within a specified timeframe. The expected date of this email is the start of the "booking window" found [here](#). If the email cannot be located (check junk mail), please email testingsupport@meazurelearning.com to request that an exam grant email be resent, as ASET does not have access to this information.

Applicants who do not reconfirm their seat by the last day of the specific booking window (see [here](#)) will be unable to write the exam. As late confirmations are not possible, applicants are required to pay the full exam fee again to register for a new date. For this, please contact ASET at exams@aset.ab.ca.

Policy for Reschedules, Rewrites, and Attempts

Alberta Candidates

Candidates who wish to reschedule their exam must email the ASET Registration Department (exams@aset.ab.ca) with the new exam date. Candidates who reschedule **after the registered exam's Registration Deadline Date** are required to pay **the full exam fee** to reschedule. If the request is received **before** this deadline date, then there are no fees to reschedule.

Candidates who are required to re-write the examination must email the ASET Registration Department (exams@aset.ab.ca) with the requested exam date. ASET Staff will follow up to obtain payment of the full exam fee in order to register for the requested date.

Candidates may attempt the exam **a maximum of three times** within one year from the date of application and must pass the exam within this time period.

British Columbia and Manitoba Candidates

Please contact ASTTBC or CTTAM directly to confirm the policy for reschedules, rewrites, and exam attempts.

Exam Accommodations for Candidates with Disabilities

According to Canadian human rights legislation and test industry standards, exam developers are responsible for providing candidates with disabilities with exam accommodations where appropriate and feasible. Exam accommodations are designed to remove barriers related to individual characteristics of candidates that may prevent them from demonstrating their technical competencies on the exam. "An appropriate accommodation is one that responds to specific individual characteristics but does so in a way that does not change the construct the test is measuring or the meaning of scores."¹

Candidates with disabilities should request accommodations to write the examination at the time of application and at least **seven (7) weeks before the exam date of their choice by submitting a request in writing to the ASET, ASTTBC, or CTTAM**. To protect the integrity of the examination, documented evidence of the candidate's disability must be submitted to ASET, CTTAM, or ASTTBC along with the application form. Such evidence includes a formal detailed diagnosis of the specific disability from an appropriate professional (e.g., physician, psychologist, rehabilitation counsellor) and supporting documentation citing the need for exam accommodations and what accommodations the candidate received in the past.

ASET, CTTAM, or ASTTBC will review the candidate's written request for accommodation and determine if it can be supported. Depending on the candidate's individual needs, ASET, CTTAM, or ASTTBC may modify exam administration conditions, including exam setting, exam presentation, or the addition of individuals to the exam (e.g., readers, scribes). Each request will be reviewed on a case-by-case basis.

Below is a list of reasonable exam accommodations for candidates with a disability.*

1. *Separate Room*

A separate room is provided to candidates who due to the nature of their disability require an exam environment that minimizes distractions resulting from noise or movement or process information by talking aloud.

2. *Additional Time*

Extending additional time to candidates is a frequently used exam accommodation that is used with a variety of disability-related conditions. Often candidates are offered time- and-one-half to complete the exam (e.g., a 3-hour exam is extended to 4.5 hours).

3. *Interpreter*

Candidates with hearing impairment may request an interpreter who has proficiency in sign language.

4. *Reader*

A reader is an individual who reads exam instructions and/or exam questions to a candidate. Candidates with visual impairment or those with a learning disability may benefit from services of a reader during the examination.

5. *Recorder*

A recorder is an individual who fills in the answers for a candidate who has difficulty writing independently.

***All costs related to exam accommodations will be the responsibility of the candidate.**

¹ American Educational Research Association (2014). *Standards for Educational and Psychological Testing*. Washington, DC (p. 67)

Examination Content

The Technical Examination – Electronics Engineering Technology tests candidates' competencies in four areas ([see Appendix A for detailed information on examination content](#)).

1. **Technical Analysis:** In this competency area, candidates are expected to be able to apply electronics knowledge, equipment, and tools to analyze technical problems and provide solutions that meet technical specifications, industry standards, local codes, and requirements of internal and external clients.
2. **Technical Design:** This competency area deals with candidates' ability to design under supervision; electronic components and systems that meet technical specifications, regulatory requirements, industry standards, local codes, and requirements of internal and external clients.
3. **Technical Evaluation:** In this competency area candidates are expected to be able to install, maintain, troubleshoot, and evaluate electronic components and systems in accordance with technical specifications, regulatory requirements, industry standards, local codes, and requirements of internal and external clients.
4. **Project Coordination:** This competency area deals with candidates' ability to assist in the implementation of projects to ensure the quality of deliverables, customer satisfaction, and adherence to schedules and budgets.

Exam questions will also vary in cognitive level. *Knowledge* questions require that candidates recall information and provide their interpretation. *Application* questions require that candidates apply their knowledge to practical situations, while *Critical thinking* questions require that candidates analyze complex situations and provide solutions. There will also be a percentage of source-based questions that may include an image, figure, tables, schematics, etc. [Refer to Appendix A for more detailed information.](#)

Study Resources for Examination

The following resources may be of use to candidates interested in refreshing their knowledge prior to writing the examination. **Candidates are not expected to study each of these resources as the technical examination is designed to test entry-level competencies related to the practice of electronics engineering technology. This list is to show some of the sources used to create items for the examination.** Rather, candidates may wish to review content areas in which they feel they would like to update their current knowledge. Detailed information on the content areas covered on the exam will be found in *Appendix A*.

Dorf, R. C., & Svoboda, J. A. (2010). Introduction to electric circuits. Hoboken, NJ: John Wiley & Sons.

Frenzel, L. E. (2008). Principles of electronic communication systems. Boston, MA: McGraw- Hill.

Manko, H. H. (1998). Soldering handbook for printed circuits and surface mounting (2nd ed.). Van Nostrand Reinhold, Netherlands: Kluwer Academic Publishers.

Miller, G. M. & Beasley, J. S. (2002). Modern electronic communication (7th ed.). Upper Saddle River, NJ: Pearson Education.

Paynter, B., & Boydell, T. (2009). Electronics technology fundamentals: Conventional flow (3rd ed.) Upper Saddle River, NJ: Pearson Education.

Rapid Tables Online Reference & Tools. (2014). Electrical Symbols & Electronic Symbols. Retrieved from http://www.rapidtables.com/electric/electrical_symbols.htm

Schultz, M. E. (2011). Grob's basic electronics. New York, NY: McGraw-Hill.

Streib, W. J. (1990). Digital circuits. South Holland, IL: Goodheart-Willcox.

Tocci, R. J., Widmer, N. S., & Moss, G. L. (2005). Digital systems: Principles and applications. Upper Saddle River, NJ: Pearson/Prentice Hall.

Wang, L., Chang, Y., & Cheng, K. (2009). Electronic design automation. Burlington, MA: Morgan Kauffman Publishers.

Wheeler, P. E. (1989). Electronic fundamentals. Saint Joseph, MI: Heathkit/Zenith Educational Systems.

Dorf, R. C., & Svoboda, J. A. (2010). Introduction to electric circuits. Hoboken, NJ: John Wiley & Sons.

Practice Exam for Purchase

Practice exam questions are available for purchase through [Meazure Learning](#). As these questions are hosted by a third-party educational partner, candidates will need to create a new account to access the practice exam. This login is **not associated** with the candidate's ASET ID.

A one-time attempt practice exam of 25 questions* is available.

*While the candidate can repurchase the same practice exam at a later date, the questions will not change.

Day of Examination

Admission to the Examination

ASET, CTTAM, and ASTTBC provide Meazure Learning with a list of examination candidates for each exam sitting. When an exam appointment is made, candidates will receive a booking confirmation email from Meazure Learning.

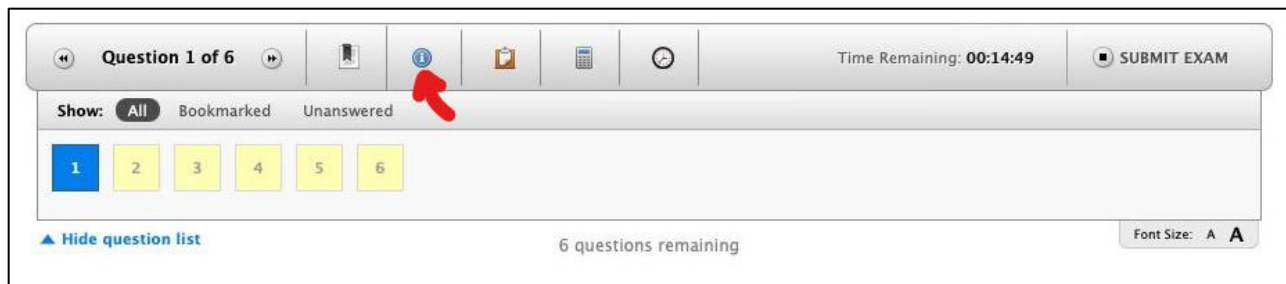
Proctors will verify your identification before allowing access to the exam platform so please have ID ready to show virtually.

Important Notes:

- If you have a non-expired temporary driver's license you must also provide another form of photo identification. The first and last names on both documents must match. It is preferred that the photo identification not be expired.
- If your only form of photo identification has expired, you must also provide another form of non-expired identification. The first and last names on both documents must match.
- If you have any other concerns about your forms of identification, please contact Meazure Learning support immediately to ensure your identification will be accepted on exam day: testingsupport@meazurelearning.com.

Permissible Items

- Disposable ear plugs (provided by candidate)
- Scrap paper and pencils (proctors will verify blank)
- Approved calculator (see policy below)
- Formula sheet will be available virtually via the ProctorU Platform accessed via the blue "i" icon (refer to screenshot) **Note:** physical copies will not be permitted or available



Prohibited Items

- Electronic devices (i.e., cell phones, pagers, digital assistants etc.)
- All types of food and beverages
- Unauthorized examination aids, assistance or collaboration materials

Calculator Policy

- Candidates can bring in a Scientific Calculator that is non-programmable, non-graphing and has no memory storage capabilities.
- Please review the [Calculator Policy](#) prior to examination day. It contains a list of approved and non-approved calculator models
- It is the candidate's responsibility to ensure their calculator is approved prior to the exam, either by being indicated on the approved list or by obtaining approval from ASET, CTTAM, or ASTTBC. If a candidate does not obtain approval prior to the exam, their calculator may be deemed inadmissible and prohibited from the exam.

Taking the Exam

At the beginning of the examination, candidates will hear verbal examination instructions from the proctor and read the Candidate's Statement of Understanding and/or Non-disclosure Agreement in the software. Failure to comply with the regulations outlined in these documents will result in the candidate's results being invalidated. Candidates will not be able to begin the examination without agreeing to the conditions outlined in the document. The assigned ID and chosen password will be used to log in when prompted by the proctor.

Next, candidates will be given written exam instructions in the software. These exam instructions will emphasize the fact that some exam questions contain images and/or require calculations. If the images appear too small on the screen, candidates will be advised to hover their mouse over them to get an expanded view. Following the exam instructions, there will be a tutorial available to candidates before they proceed to the exam.

The exam is closed book; a formula sheet is provided if necessary and is three hours in duration. Once a candidate starts an exam, they must complete the exam they have started. If the candidate notices they have started the wrong examination, they must notify the proctor within the first 5 minutes of the exam sitting to switch to the correct exam.

Upon submitting their exam responses, candidates will be offered an opportunity to provide feedback on exam material and exam administration conditions by completing a short online survey. **Results of the exam are not provided to the candidates at this time.**

After the Examination

Examination Scoring

Multiple-choice examination questions are scored dichotomously, using a score of "0" for an incorrect response and a score of "1" for a correct response. The Technical Examinations are criterion-referenced exams, which means that a candidate should obtain a score that is equal or higher than an exam pass mark to pass the examination.

Pass Mark

Each examination has its own pass mark. The pass mark for the Technical Examinations is determined by the Exam Committee, which considered the difficulty of exam questions and the expected level of performance for a minimally competent engineering technologist. A psychometrically acceptable standard-setting methodology was used to set examination pass marks.

The pass mark for the *Technical Examination – Electronics Engineering Technology* is 67%.

Results

Candidates are emailed their exam results **within two to three weeks after the exam date**. Exam results are reported to the candidate as "pass" or "fail".

Unsuccessful candidates will also receive a performance report indicating a failure to pass, their score, and areas of strength and weakness in the four tested competency areas. The unsuccessful candidates will be able to retake the exam. Please refer to the Rewrite Policy section in this handbook to schedule the next exam.

Review and Appeal Process

A candidate who fails the examination may request that their exam score be verified. Due to the automated scoring and extensive quality control procedures, errors in scoring are extremely unlikely. However, candidates may request that ASET, CTTAM, or ASTTBC manually rescore their exam to verify the original score.

Appendix A: Electronics Engineering Technology - Technical Exam Blueprint

Electronics Engineering Technology Technical Examination Blueprint		
Competency	Multiple Choice Questions	
	Target Range (% of Qs)	Target Range (# of Qs)
1. Technical Analysis	30-40%	30-40
1.1 Collect quantitative and qualitative information to better understand technical problems and develop solutions.		
1.2 Identify the scope of work in consultation with one's supervisor.		
1.3 Assess site safety before collecting data.		
1.4 Identify appropriate technical tests and criteria.		
1.5 Conduct appropriate technical tests of electronic components and systems.		
1.6 Consult equipment manuals and data sheets.		
1.7 Examine analog and digital circuits using appropriate instrumentation, such as spectrum analyzers, logic analyzers and oscilloscopes.		
1.8 Consider the limitations of data collection methods for conclusions drawn from test results.		
1.9 Assess information to develop solutions and identify opportunities for improvements.		
1.10 Calibrate equipment to improve accuracy.		
1.11 Perform calculations to determine expected outcomes.		
1.12 Compare test results to technical specifications, regulatory requirements, industry standards, local codes, and requirements of internal and external clients.		
1.13 Use appropriate software or programs to analyze results.		
1.14 Record test results using appropriate media (e.g., an electronic spreadsheet).		
1.15 Develop technical solutions.		
1.16 Evaluate advantages and disadvantages of technical solutions.		
1.17 Document technical solutions for approval and implementation.		
2. Technical Design	15-25%	15-25
2.1 Assist in creating technical specifications for electronic components.		
2.2 Comply with standards and codes that apply to the design.		
2.3 Identify potential technical, health, and environmental risks associated with the implementation of specific technical solutions.		
2.4 Interpret circuit schematics and drawings of electronic components and systems.		
2.5 Create circuit schematics and drawings of electronic components and systems.		
2.6 Apply electronics knowledge, such as: 1) Digital circuits; 2) Analog circuits; 3) Microprocessors; 4) Microcontrollers; 5) Communication protocols; 6) Radiofrequency circuits; 7) Logic systems; 8) Control systems.		
2.7 Apply programming skills, such as: 1) Assembly language; 2) C-language.		
2.8 Assist in building prototypes to verify the design.		
2.9 Set up and operate test equipment to evaluate prototypes.		
2.10 Evaluate prototypes to ensure compliance with technical specifications.		

2.11 Design equipment layout, such as rack configuration.		
2.12 Collaborate with a multi-disciplinary team to solve technical problems.		
3. Technical Evaluation	30-40%	30-40
3.1 Observe lock-out and tag-out procedures.		
3.2 Identify safety hazards associated with installation, maintenance, troubleshooting, and repairs of electronic equipment.		
3.3 Use certified and calibrated equipment to install, maintain, troubleshoot or repair electronic components and systems.		
3.4 Perform supervised design validation by comparing technical specifications to the requirements of internal and external clients.		
3.5 Perform supervised design verification by testing electronic systems against technical specifications: 1) Prototype tests; 2) Functional and/or operational tests; 3) Tests specified by industry standards and/or regulatory		
3.6 Maintain records of the design validation and verification procedures.		
3.7 Conduct the installation and operation of electronic equipment and systems such as: 1) Analog and digital communication circuits and systems; 2) Digital networks; 3) Microprocessor controlled equipment and programmable logic controllers; 4) Computers, computer controlled equipment and computer networks; 5) Radio and television broadcasting equipment; 6) Instrumentation; 7) Electronic sensors, circuits and systems involved in manufacturing processes.		
3.8 Repair defective or improperly functioning electronic components and systems using appropriate tools.		
3.9 Perform supervised commissioning of electronic components and systems after repairs and installation.		
3.10 Perform preventative maintenance of electronic components and systems using technical specifications.		
3.11 Provide feedback on maintenance schedules.		
3.12 Perform calibration of electronic components and systems.		
3.13 Report situations where technical specifications do not apply to the field.		
3.14 Troubleshoot electrical connections (e.g., cables and connectors).		
3.15 Troubleshoot microprocessors, electronic instruments, equipment, or systems, using electronic test equipment, such as logic analyzers.		
3.16 Use a systematic approach to identifying faults when troubleshooting.		
3.17 Maintain accurate and complete documentation on maintenance, installation, and troubleshooting activities (e.g., work orders).		
3.18 Provide technical support and recommendations for installation, maintenance, upgrades, or enhancements.		
3.19 Verify that technical documentation is written in accordance with organizational quality control standards.		
3.20 Assist in identifying the non-conforming electronic components and systems.		
3.21 Provide sketches of field changes and discrepancies for engineering corrections and drawings.		
3.22 Assist in identifying lessons learned in the project.		
3.23 Create technical procedures for new equipment.		
4. Project Coordination	5-15%	5-15
4.1 Assist in identifying parts, material, and equipment for projects.		
4.2 Prioritize own work activities to ensure that project objectives are met on time and on budget.		
4.3 Research equipment or component needs, sources, competitive prices, delivery times, or operational costs.		
4.4 Assist in calculating cost, material, and resource estimates for projects.		
4.5 Prepare schedules for deliverables.		
4.6 Quantify the work that is completed to-date.		
4.7 Assist in monitoring compliance with technical specifications.		
4.8 Assist in communicating project information to internal and external clients.		

4.9 Establish and maintain effective working relationships with internal and external clients.		
4.10 Explain the value of workplace safety legislation.		
4.11 Comply with workplace safety legislation.		
4.12 Complete project management documentation, such as progress reports.		
Total	100%	100

Cognitive Level	Multiple-choice Questions	
	Target Range (% of Qs)	Target Range (# of Qs)
Knowledge	40-50%	40-50
Application	40-50%	40-50
Critical Thinking	5-15%	5-15
Total	100%	100

Question Type	Multiple-choice Questions	
	Target Range (% of Qs)	Target Range (# of Qs)
Questions with Images	35-45%	35-45
Questions without Images	55-65%	55-65
Total	100%	100